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## BIB DATA SHEET

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**APPLICANTS**

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**\*\* CONTINUING DATA \*\*\*\*\***

This application is a 371 of PCT/IB04/52595 11/30/2004

**\*\* FOREIGN APPLICATIONS \*\*\*\*\***

EUROPEAN PATENT OFFICE (EPO) 03104704.6 12/16/2003

**\*\* IF REQUIRED, FOREIGN FILING LICENSE GRANTED \*\***

04/03/2007

Foreign Priority claimed	<input checked="" type="checkbox"/> Yes <input type="checkbox"/> No	STATE OR COUNTRY	SHEETS DRAWINGS	TOTAL CLAIMS	INDEPENDENT CLAIMS
35 USC 119(a-d) conditions met	<input checked="" type="checkbox"/> Yes <input type="checkbox"/> No	<input type="checkbox"/> Met after Allowance			
Verified and Acknowledged	/COREY S FAHERTY/ Examiner's Signature	Initials	BELGIUM	4	13

**ADDRESS**

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**TITLE**

Memory-efficient instruction processing scheme

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